

JG979 U.S. PTO  
10/05/383  
01/22/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

HOLGER THIEL ET AL

PHDE 010015

Serial No.:

Filed: CONCURRENTLY

Title: CIRCUIT ARRANGEMENT

Commissioner for Patents  
Washington, D.C. 20231

5/Pm a  
J. Steptoe  
6-20-02

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,  
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

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4. (Amended) A circuit arrangement as claimed in claim 2,  
characterized in that the first temporal delays ( $\Delta t_1$ )

*Al  
Cont* generated in the first delay unit (24) and/or the second  
temporal delays ( $\Delta t_2$ ) generated in the second delay unit (34)  
can each be built up with gate delay times.

5. (Amended) A circuit arrangement as claimed in claim 1 ,  
characterized in that the second modulation signal ( $M_2$ ) is  
temporally shifted with respect to the first modulation